

Features

- ※ 13.2 - 450V input voltage range
- ※ Adjustable 1.20 - 440V output regulation
- ※ 5% output voltage tolerance
- ※ Output current limiting
- ※ 10 μ A typical ADJ current
- ※ Internal junction temperature limiting

Applications

- ※ Off-line SMPS startup circuits
- ※ Adjustable high voltage constant current source
- ※ Industrial controls
- ※ Motor controls
- ※ Battery chargers
- ※ Power supplies

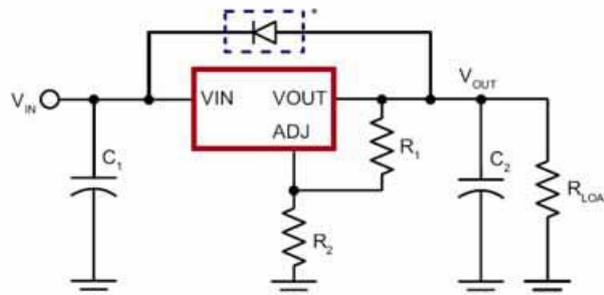
General Description

The Top-Best IC PT \hat{G} FF is a high voltage, low output current, adjustable linear regulator. It has a wide operating input voltage range of 13.2 - 450V. The output voltage can be adjusted from 1.20 - 440V provided that the input voltage is at least 12V greater than the output voltage. The output voltage can be adjusted by means of two external resistors R1 and R2 as shown in the typical application circuits. The PT \hat{G} FF regulates the voltage difference between V_{OUT} and ADJ pins to a nominal value of 1.20V. The 1.20V is amplified by the external resistor ratio R1 and R2. An internal constant bias current of typically 10 μ A is connected to the ADJ pin. This increases V_{OUT} by a constant voltage of 10 μ A times R2.

The PT \hat{G} FF has current limiting and temperature limiting. The output current limit is typically 20mA and the minimum temperature limit is 125°C. An output short circuit current will therefore be limited to 20mA. When the junction temperature reaches its temperature limit, the output current and/or output voltage will decrease to keep the junction temperature from exceeding its temperature limit. For SMPS start-up circuit applications, the PT \hat{G} FF turns off when an external voltage greater than the output voltage of the PT \hat{G} FF is applied to V_{OUT} of the PT \hat{G} FF. To maintain stability, a bypass capacitor of 1.0 μ F or larger and a minimum DC output current of 500 μ A are required.

The device is available in SOT-89, TO-252 (D-PAK), and TO-92 packages.

Typical Application Circuit



Required for conditions where V_{IN} is less than V_{OUT}

Absolute Maximum Ratings

Parameter	Value
V _{IN} input voltage (voltages ref to ADJ)	-0.5V to +480V
Output voltage range	-0.5V to +470V
Operating ambient temperature range	-40°C to +85°C
Operating junction temperature range	-40°C to +125°C
Storage temperature range	-65°C to +150°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Pin Configuration



Electrical Characteristics (Test conditions unless otherwise specified: -40°C < T_A < 85°C.)

Sym	Parameter	Min	Typ	Max	Units	Conditions
V _{IN} - V _{OUT}	Input to output voltage difference	12	-	450	V	---
V _{OUT}	Overall output voltage regulation	1.14	1.20	1.26	V	13.2V < V _{IN} < 400V, R1 = 2.4KΩ, R2 = 0
V _{OUT}	Overall output voltage regulation	375	400	426	V	R1 = 2.4KΩ, R2 = 782KΩ
ΔV _{OUT}	Line regulation	-	0.00 3	0.01	%/V	17V < V _{IN} < 400V, V _{OUT} = 5V, I _{OUT} = 0.5mA
ΔV _{OUT}	Load regulation	-	1.4	3.0	%	V _{IN} = 17V, V _{OUT} = 5V, 0.5mA < I _{OUT} < 10mA
ΔV _{OUT}	Temperature regulation	-1	-	+1	%	V _{IN} = 17V, V _{OUT} = 5V, I _{OUT} = 10mA, -40°C < T _A < 85°C
I _{OUT}	Output current limit	10	-	30	mA	T _J < 85°C, V _{IN} - V _{OUT} = 12V
I _{OUT}	Output current limit	-	-	0.5	mA	T _J < 125°C, V _{IN} - V _{OUT} = 450V
I _{OUT}	Minimum output current	-	0.3	0.5	mA	Includes R1 and load current
I _{ADJ}	Adjust output current	5.0	10	15	uA	---
C2	Minimum output load capacitance	1.0	-	-	uF	---
DV _{OUT} / DV _{IN}	Ripple rejection ratio	50	60	-	dB	120Hz, V _{OUT} = 5V
T _{LIMIT}	Junction temperature limit	125	-	-	°C	---

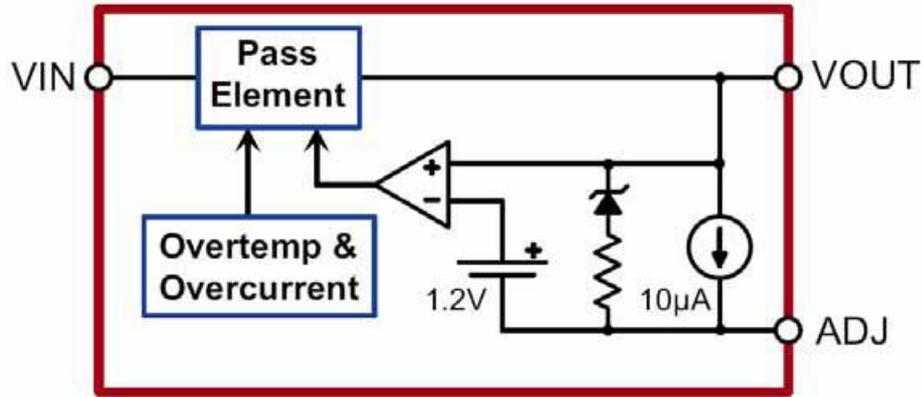
Thermal Characteristics

Package	Power Dissipation @ T _A = 25°C	θ _{jc} °C/W	θ _{ja} °C/W
TO-92	0.74W	125	170
TO-89	1.6W	15	78†
TO-252	2.5W	6.25	50†

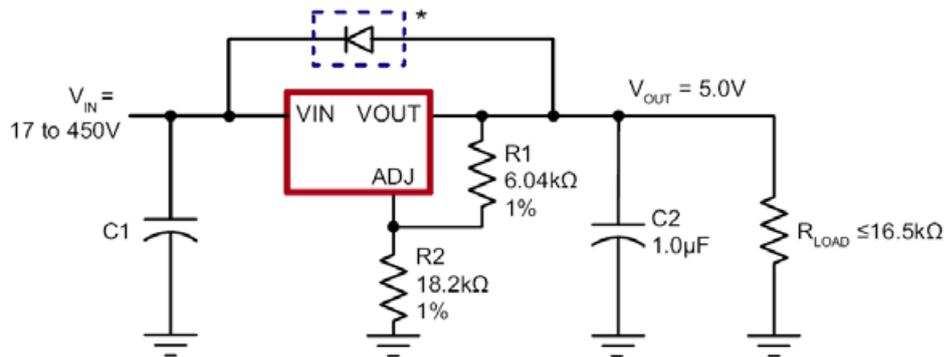
Note:

† Mounted on FR4 board, 25mm x 25mm x 1.57mm.

Functional Block Diagram



Typical Application Circuits



Required for conditions where V_{IN} is less than V_{OUT} , $V_{OUT} = 1.2V * \left(1 + \frac{R2}{R1}\right) + I_{ADJ} * R2$

Figure 1: High Input Voltage, 5.0V Output Linear Regulator

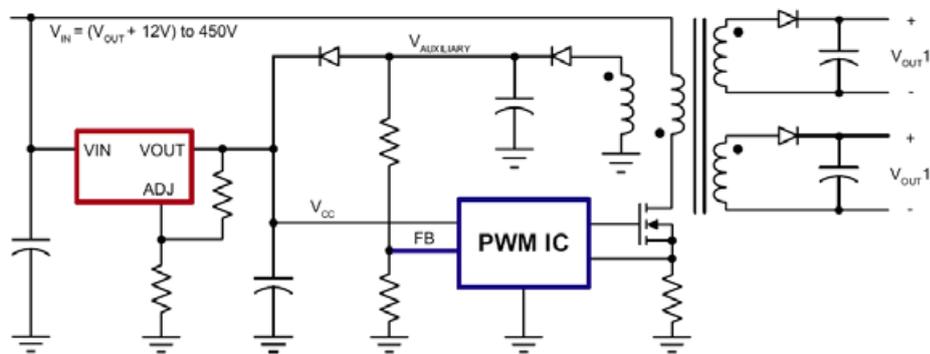


Figure 2: SMPS Start-Up Circuit

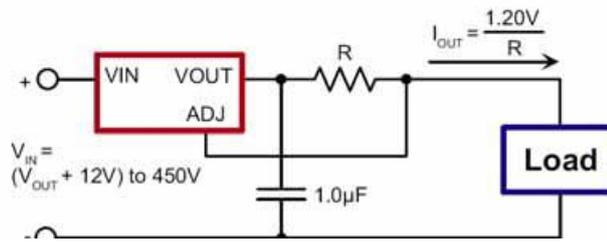
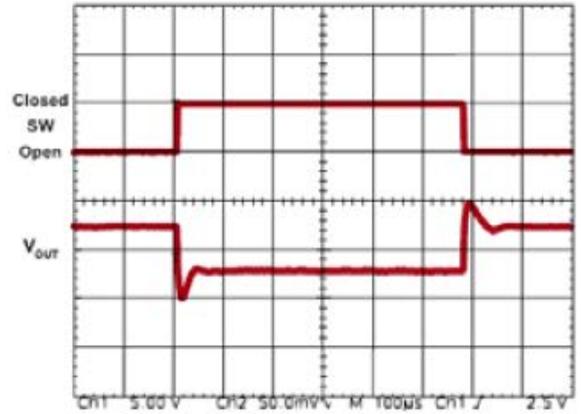
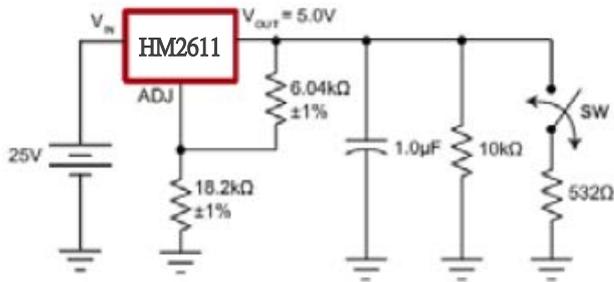


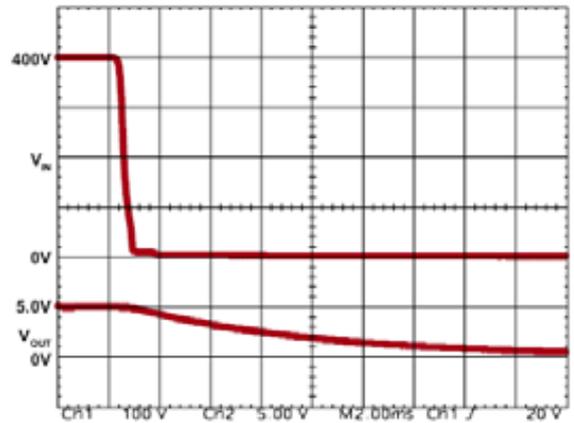
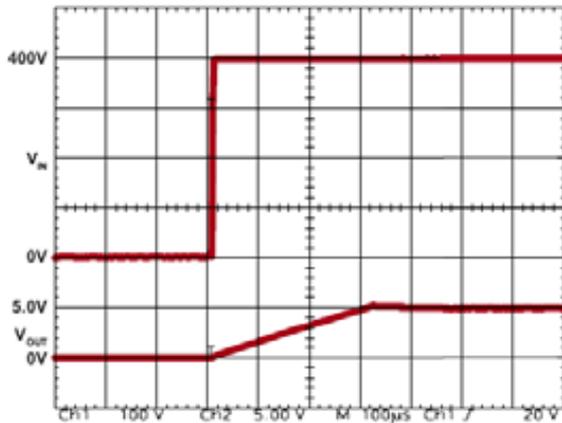
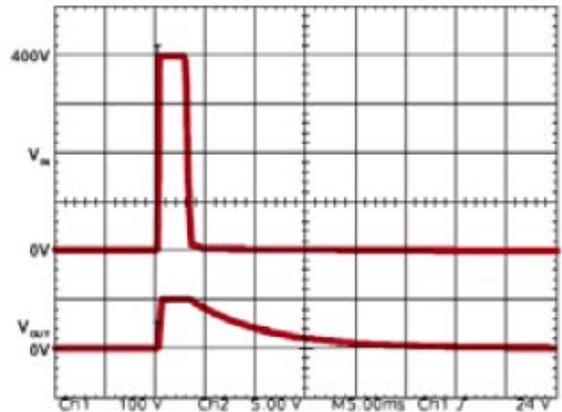
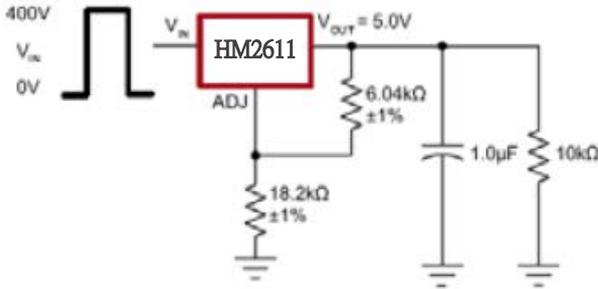
Figure 3: High Voltage Adjustable Constant Current Source

Typical Performance Curves

Load Transient Response

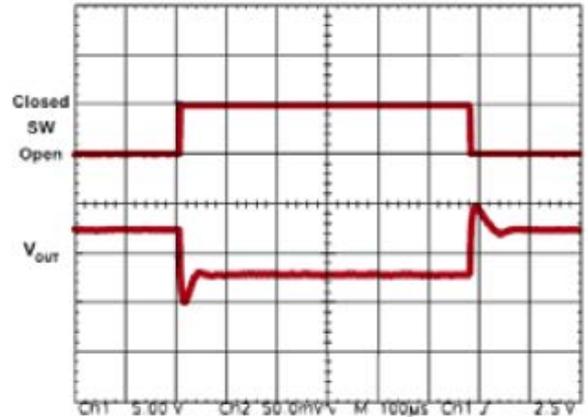
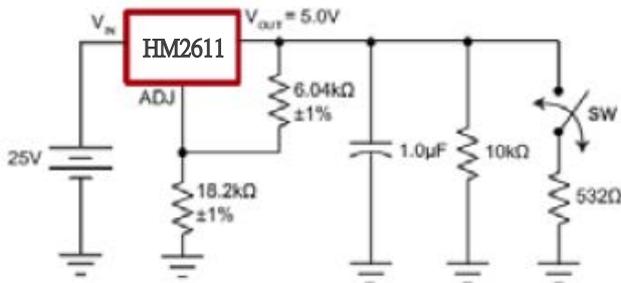


Line Transient Response

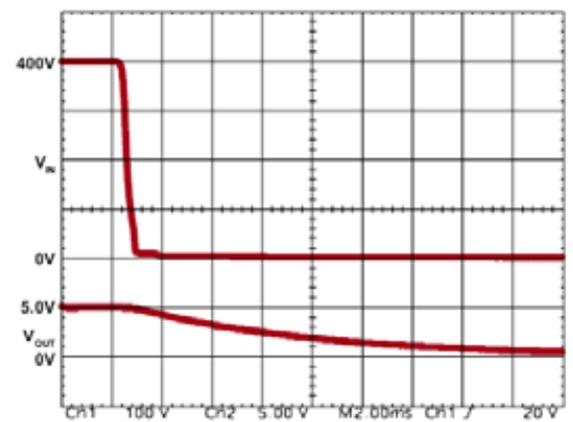
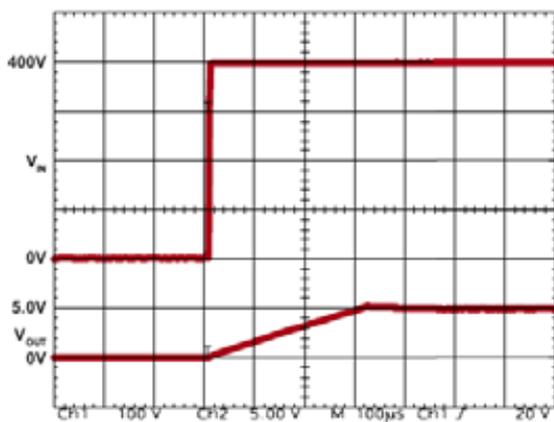
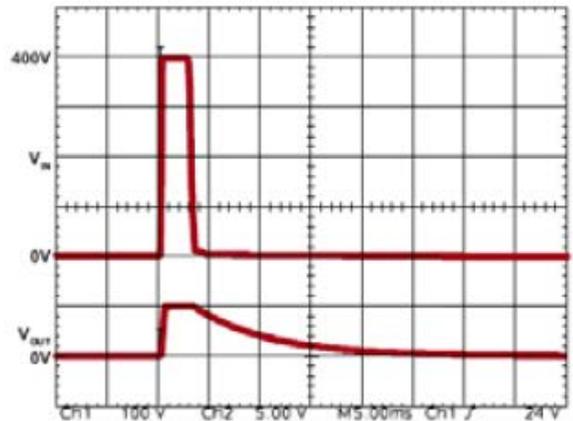
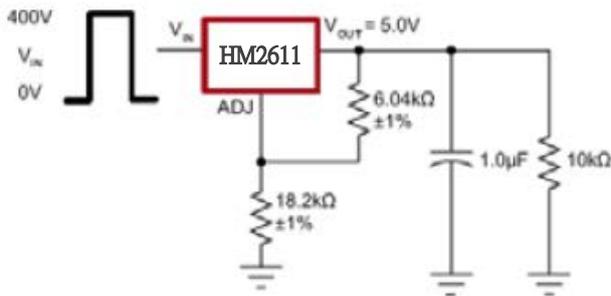


Typical Performance Curves (cont.)

Load Transient Response

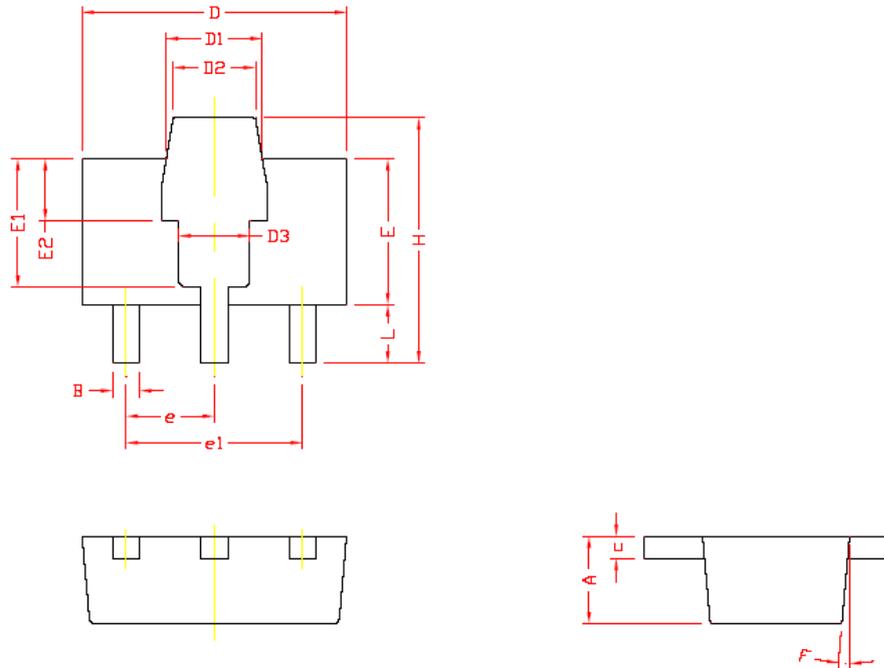


Line Transient Response



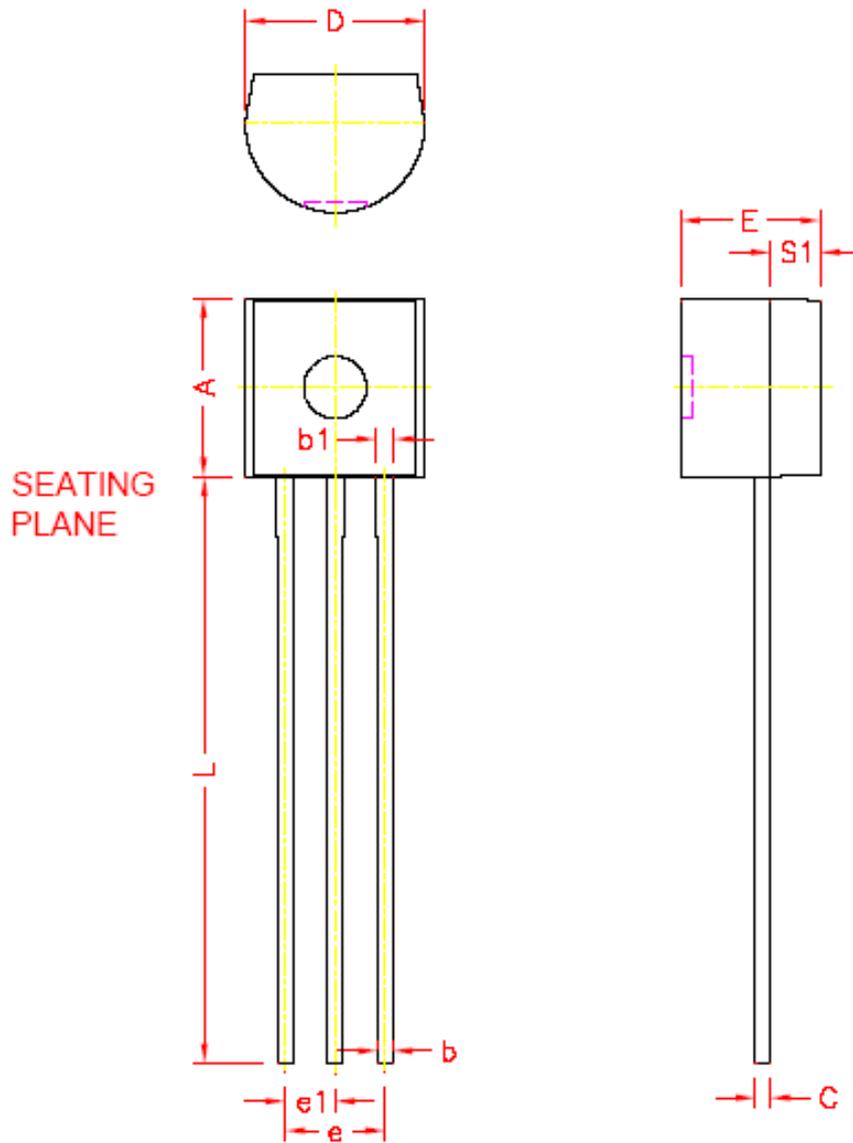
Package Information

SOT-89



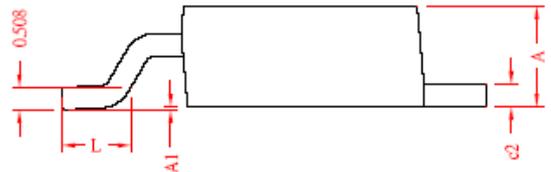
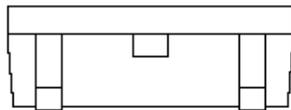
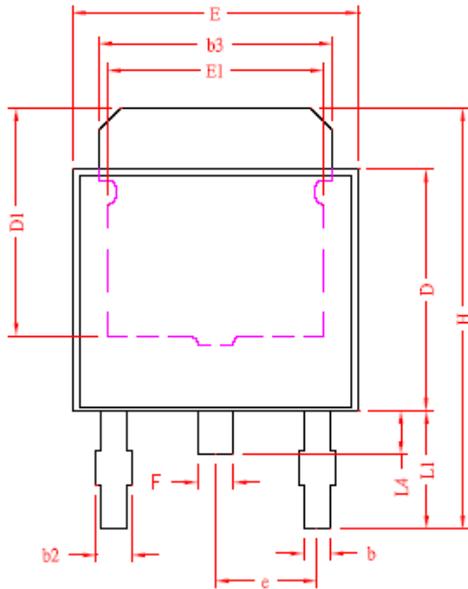
REF.	millimeters	
	Min.	Max.
A	1.40	1.60
B	0.40	0.52
c	0.35	0.41
D	4.40	4.60
D1	1.50	1.70
D2	1.30	1.50
D3	1.10	1.30
E	2.40	2.60
E1	2.20 REF.	
E2	1.06 REF.	
e	1.50 REF.	
e1	3.00 REF.	
F	5° TYP.	
H	4.05	4.25
L	0.89	1.20

TO-92



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.45	4.7	D	4.44	4.7
S_1	1.02	—	E	3.30	3.81
b	0.36	0.51	L	12.70	—
b_1	0.36	0.76	e_1	1.15	1.39
C	0.36	0.51	e	2.42	2.66

TO-252



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.20	2.40	E	6.40	6.80
A1	0	0.15	EI	3.81	---
b	0.50	0.70	e	2.30 REF.	
b2	0.60	0.90	F	0.70	0.90
b3	5.20	5.50	H	9.40	10.20
c2	0.45	0.55	L	1.40	1.77
D	5.40	5.80	L1	2.40	3.00
DI	4.57	---	L4	0.80	1.20