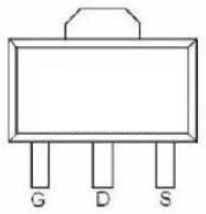
HM2N50PR

Silicon N-Channel Power MOSFET

V_{DSS}	500	V
I_D	2	A
P_D (T_C=25℃)	2.5	W
$R_{DS(ON)TYP}$	5.0	Ω



SOT-89-3L top view

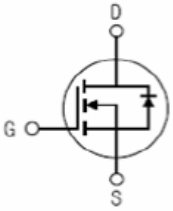
General Description：

HM2N50PR the silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is SOT-89-3L, which accords with the RoHS standard.

Features：

- **Fast Switching**
- **Low ON Resistance(Rdson≤3.2Ω)**
- **Low Gate Charge (Typical Data:5nC)**
- **Low Reverse transfer capacitances(Typical:4.5pF)**
- **100% Single Pulse avalanche energy Test**

Inner Equivalent Principium Chart



Applications：

Power switch circuit of LCD Power and adaptor.

Absolute（Tc= 25℃ unless otherwise specified）：

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-to-Source Voltage	500	V
I_D	Continuous Drain Current	2	A
	Continuous Drain Current T_C = 100 °C	1.4	A
I_{DM} [a1]	Pulsed Drain Current	6	A
V_{GS}	Gate-to-Source Voltage	±25	V
E_{AS} [a2]	Single Pulse Avalanche Energy	50	mJ
E_{AR} [a1]	Avalanche Energy ,Repetitive	6.4	mJ
I_{AR} [a1]	Avalanche Current	1.2	A
dv/dt [a3]	Peak Diode Recovery dv/dt	5	V/ns
P_D	Power Dissipation	2.5	W
	Derating Factor above 25°C	0.02	W/℃
T_J， T_{stg}	Operating Junction and Storage Temperature Range	150，–55 to 150	℃
T_L	MaximumTemperature for Soldering	300	℃

Electrical Characteristics（Tc= 25℃ unless otherwise specified）：

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	500	--	--	V
$\Delta BV_{DSS}/\Delta T_J$	Bvdss Temperature Coefficient	ID=250uA,Reference25℃	--	0.7	--	V/℃
I_{DSS}	Drain to Source Leakage Current	V_{DS} =500V, V_{GS}= 0V, T_a = 25℃	--	--	1	μA
		V_{DS} =320V, V_{GS}= 0V, T_a = 125℃			10	
$I_{GSS(F)}$	Gate to Source Forward Leakage	V_{DS} =0V, V_{GS}= 30V	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	V_{DS} =0V,V_{GS} =-30V	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	V_{GS}=10V,I_D=1A	--	5.0	7.0	Ω
$V_{GS(TH)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	2.0	3.0	4.0	V
Pulse width tp≤380μs, δ ≤2%						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
g_{fs}	Forward Transconductance	V_{DS}=15V, I_D =1A		1		S
C_{iss}	Input Capacitance	V_{GS} = 0V V_{DS} = 25V f = 1.0MHz	--	139		pF
C_{oss}	Output Capacitance		--	21		
C_{rss}	Reverse Transfer Capacitance		--	4.5		

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	I_D =2.0A V_{DD} = 200V V_{GS} = 10V R_G = 25Ω	--	6	--	ns
tr	Rise Time		--	7	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	30	--	
t_f	Fall Time		--	9	--	
Q_g	Total Gate Charge	I_D =2.0A V_{DD} =200V V_{GS} = 10V	--	5		nC
Q_{gs}	Gate to Source Charge		--	0.9		
Q_{gd}	Gate to Drain (“Miller”)Charge		--	2.5		

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I_S	Continuous Source Current (Body Diode)		--	--	2	A
I_{SM}	Maximum Pulsed Current (Body Diode)		--	--	6	A
V_{SD}	Diode Forward Voltage	I_S=2A,V_{GS}=0V	--	--	1.5	V
trr	Reverse Recovery Time	I_S=2A,T_j = 25° C	--	60	--	ns
Qrr	Reverse Recovery Charge	dI_F/dt=100A/us, V_{GS}=0V	--	139	--	nC
Pulse width tp≤380μs, δ ≤2%						

Symbol	Parameter	Typ.	Units
$R_{\theta JC}$	Junction-to-Case	50	℃/W
$R_{\theta JA}$	Junction-to-Ambient	62.5	℃/W

[a1]: Repetitive rating; pulse width limited by maximum junction temperature
[a2]: L=10.0mH, I_D=3.2A, Start T_J=25℃
[a3]: I_{SD} =2A,di/dt ≤100A/us,V_{DD}≤$BV_{DS,}$ Start T_J=25℃

Characteristics Curve:

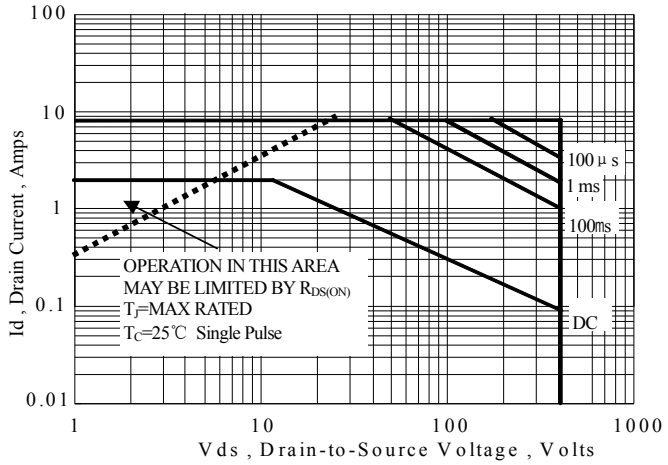


Figure 1 Maximun Forward Bias Safe Operating Area

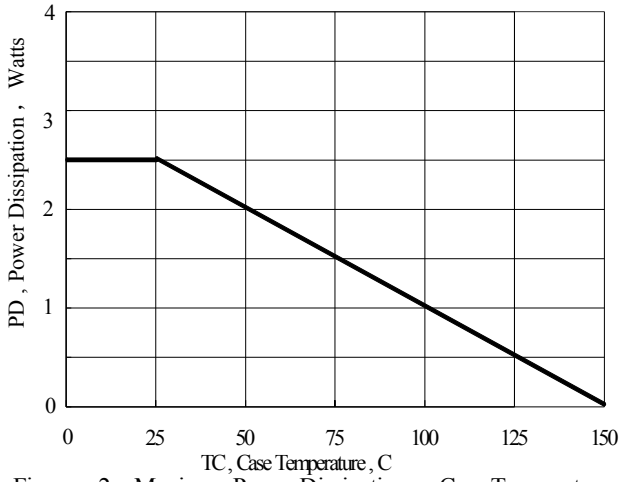


Figure 2 Maximun Power Dissipation vs Case Temperature

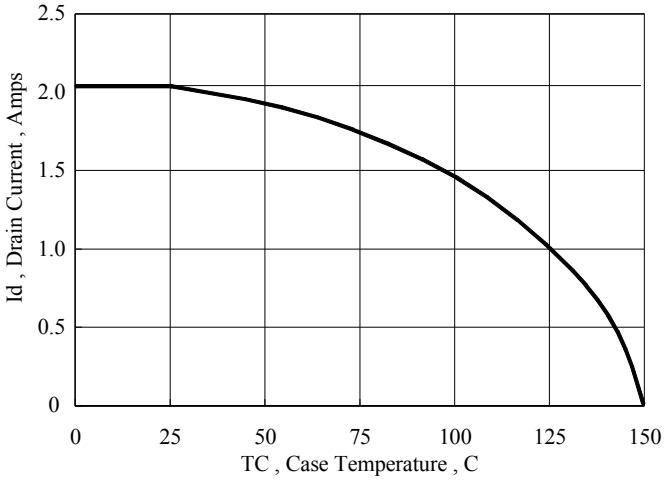


Figure 3 Maximum Continuous Drain Current vs Case Temperature

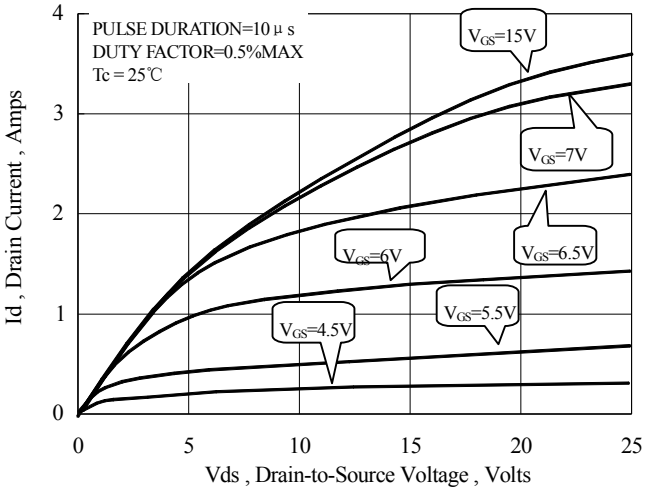


Figure 4 Typical Output Characteristics

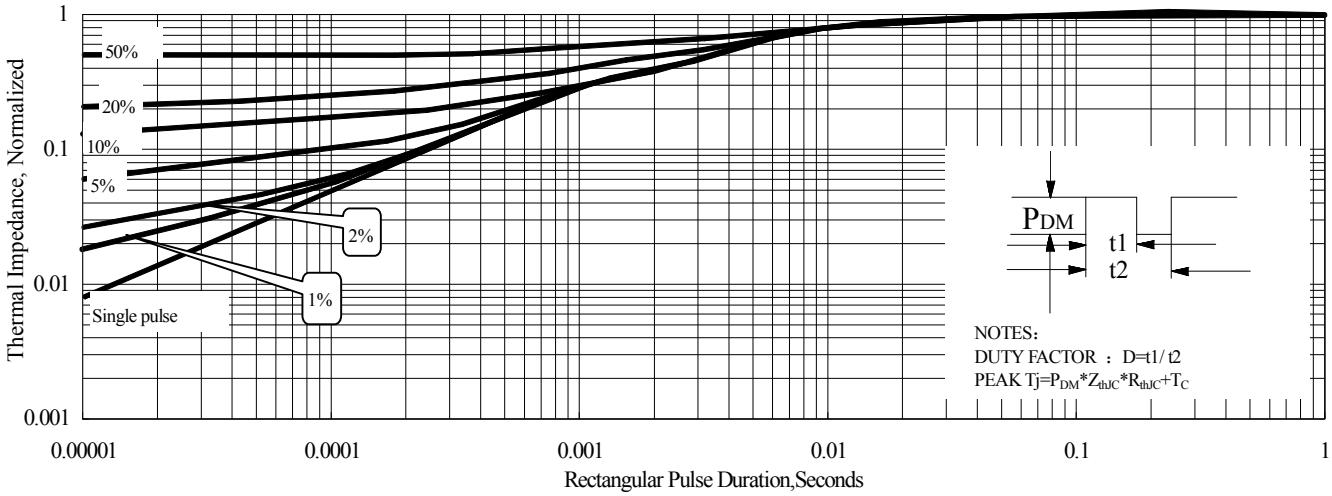


Figure 5 Maximum Effective Thermal Impendance , Junction to Case

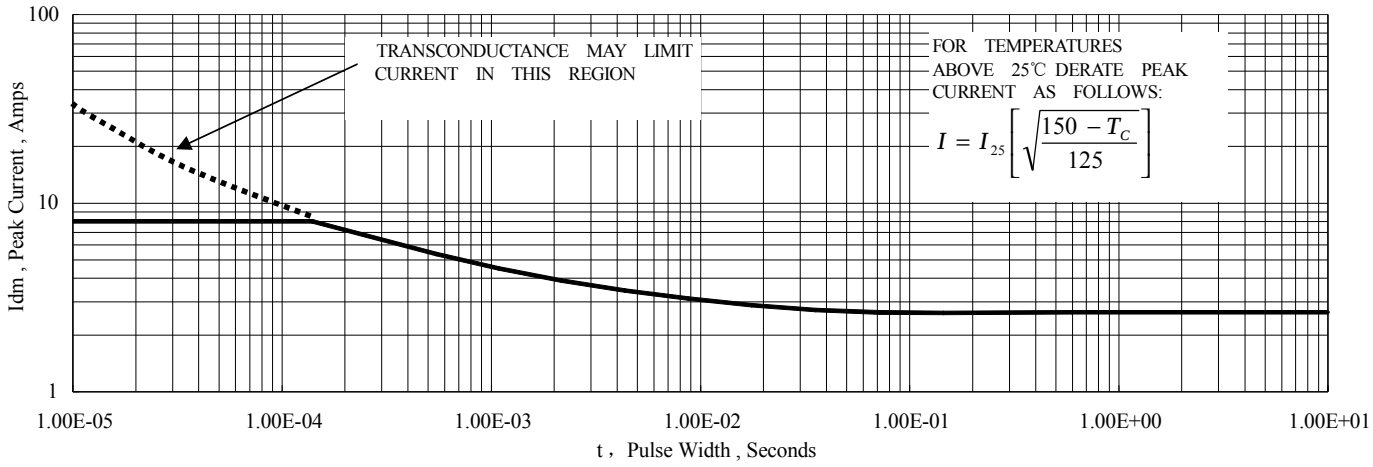


Figure 6 Maximun Peak Current Capability

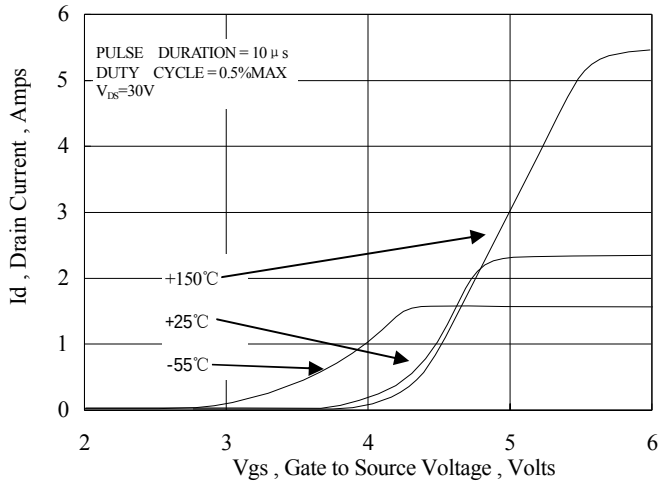


Figure 7 Typical Transfer Characteristics

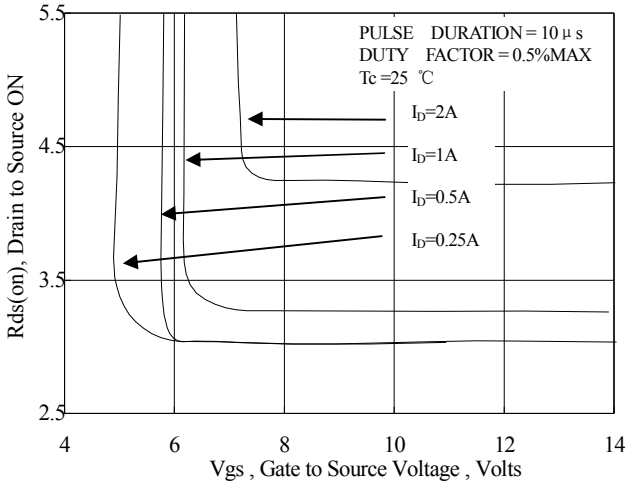


Figure 8 Typical Drain to Source ON Resistance vs Gate Voltage and Drain Current

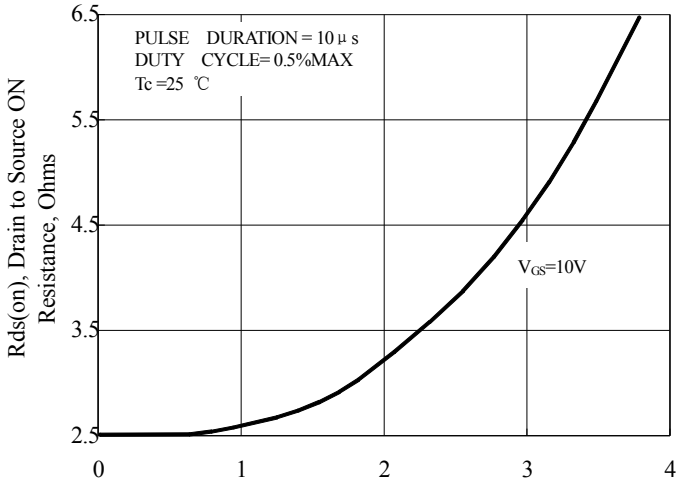


Figure 9 Typical Drain to Source ON Resistance vs Drain Current

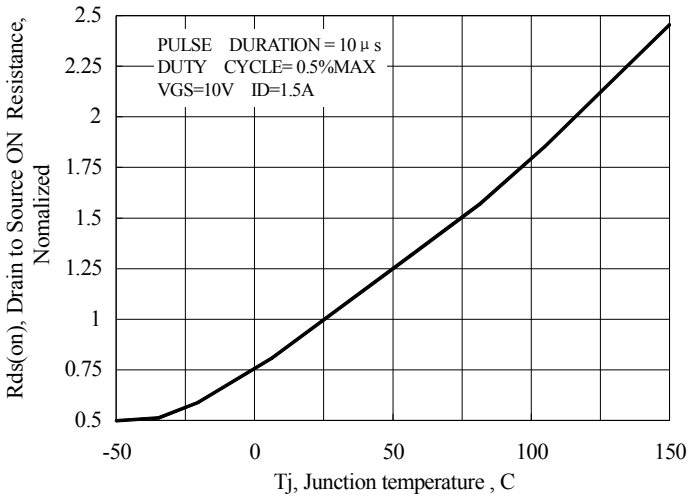


Figure 10 Typical Drian to Source on Resistance vs Junction Temperature

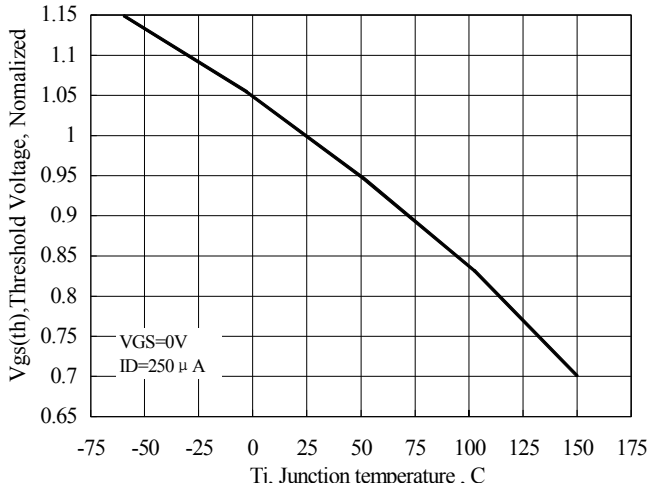


Figure 11 Typical Theshold Voltage vs Junction Temperature

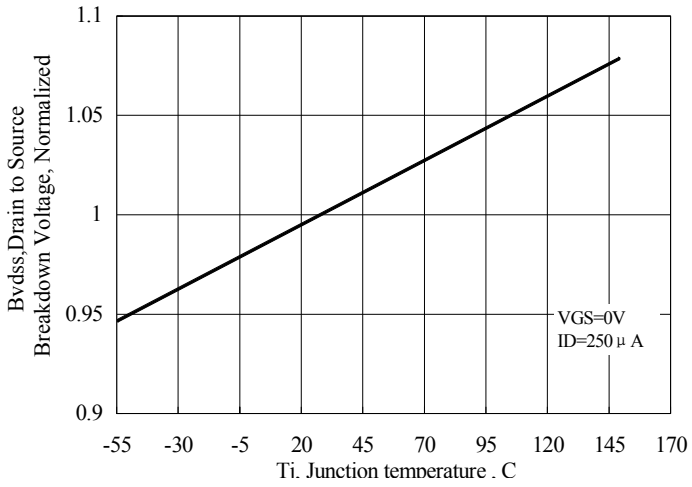


Figure 12 Typical Breakdown Voltage vs Junction Temperature

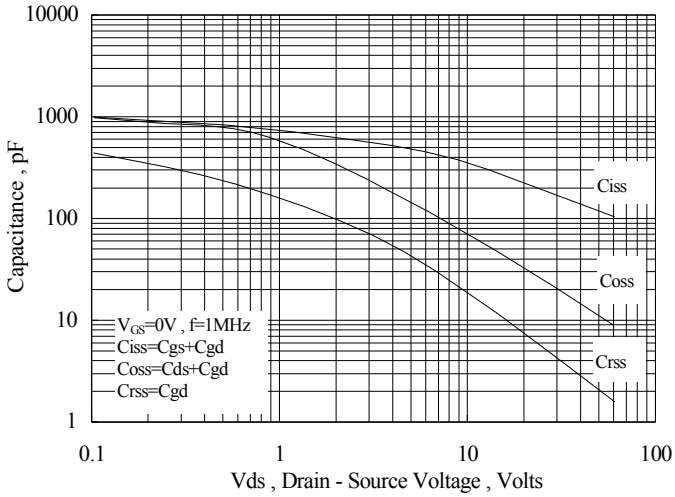


Figure 13 Typical Capacitance vs Drain to Source Voltage

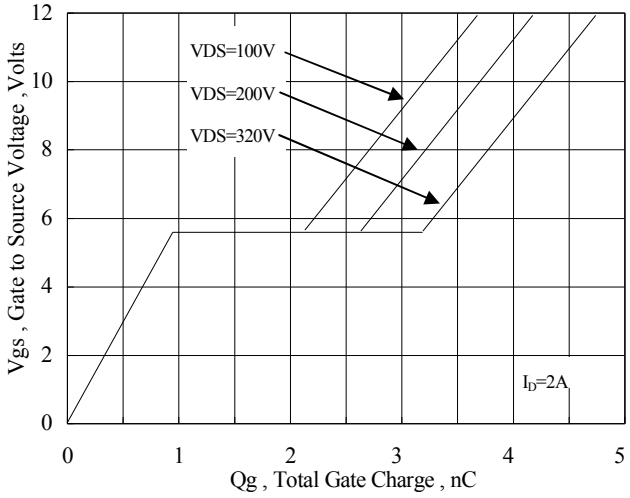


Figure 14 Typical Gate Charge vs Gate to Source Voltage

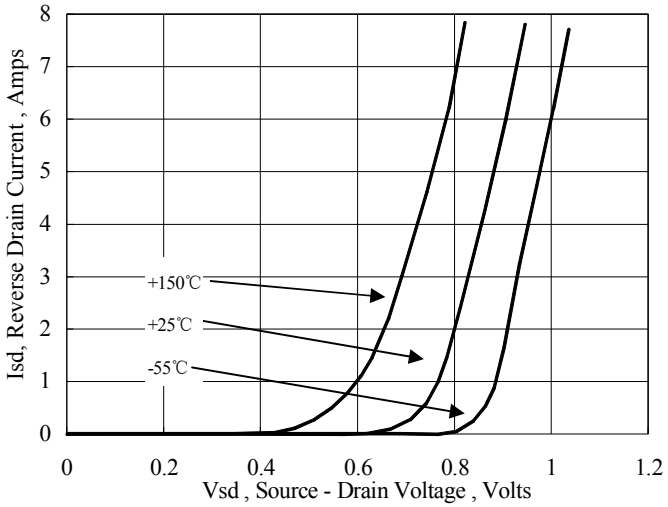


Figure 15 Typical Body Diode Transfer Characteristics

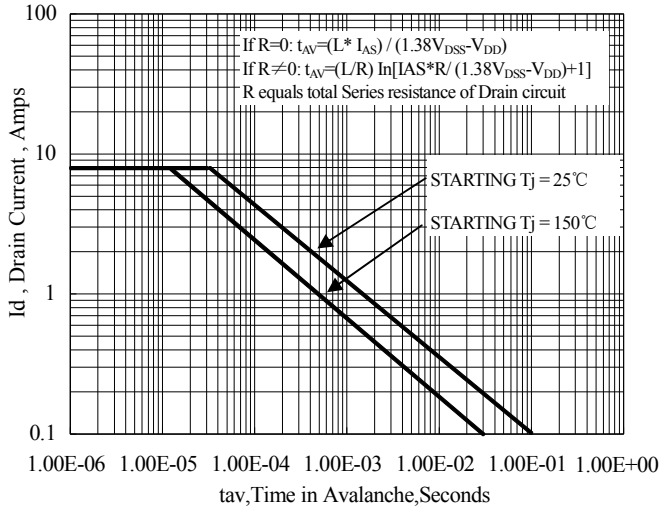


Figure 16 Unclamped Inductive Switching Capability

TestCircuitandWaveform

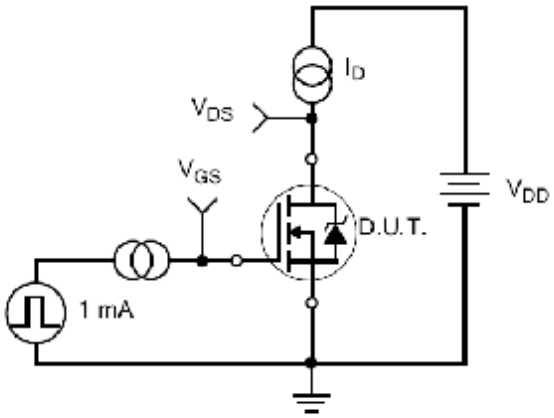


Figure 17. Gate Charge Test Circuit

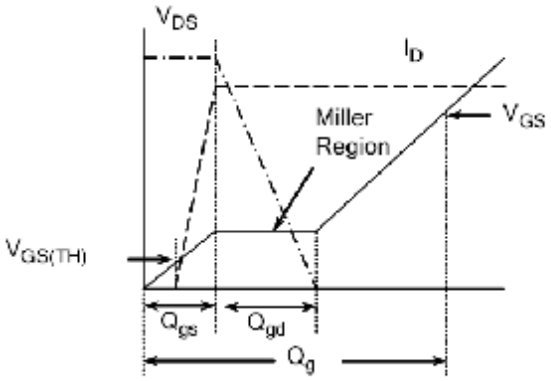


Figure 18. Gate Charge Waveform

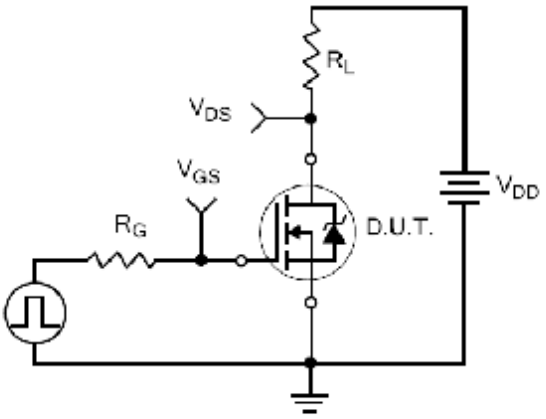


Figure 19. Resistive Switching Test Circuit

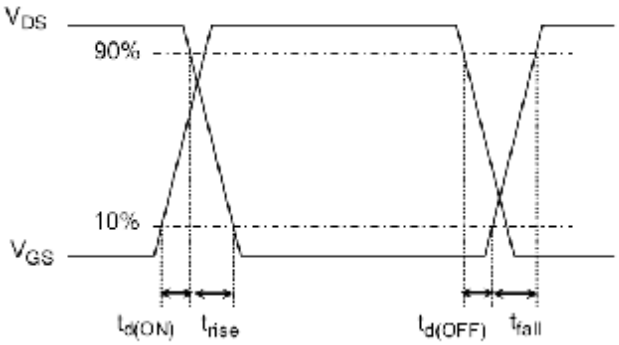


Figure 20. Resistive Switching Waveforms

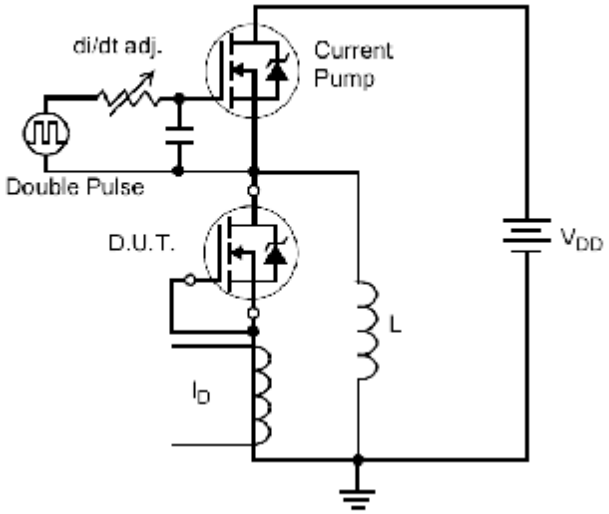


Figure 21. Diode Reverse Recovery Test Circuit

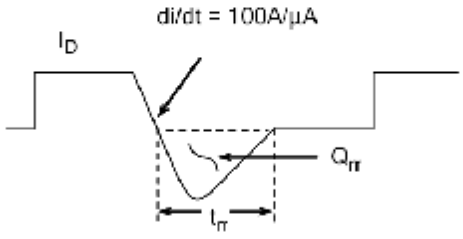


Figure 22. Diode Reverse Recovery Waveform

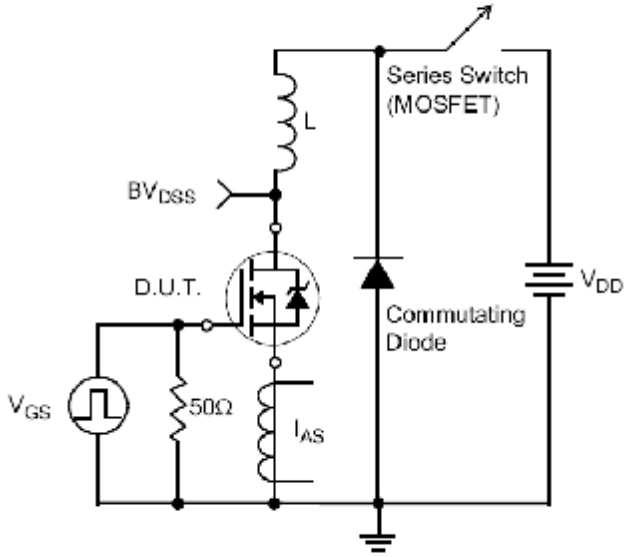
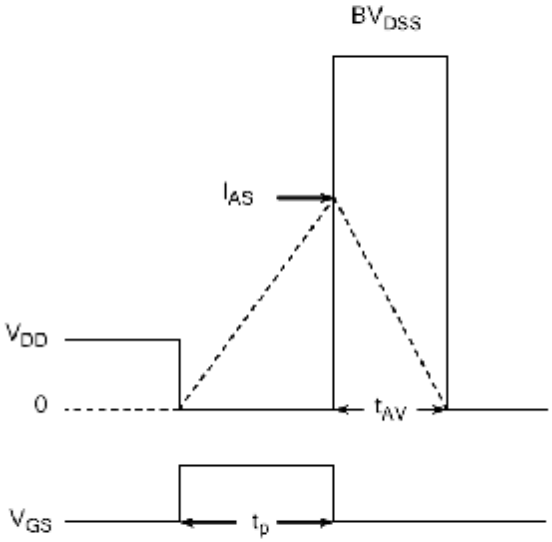


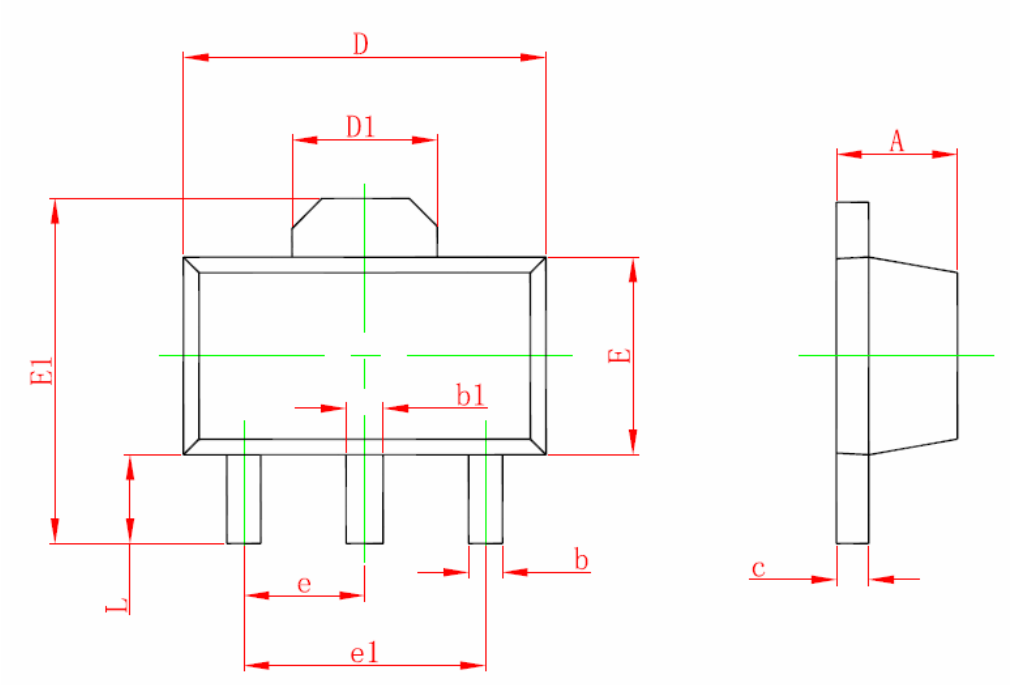
Figure 23. Unclamped Inductive Switching Test Circuit



$$E_{AS} = \frac{I_{AS}^2 L}{2}$$

Figure 24. Unclamped Inductive Switching Waveforms

SOT-89-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047

Notes

1. All dimensions are in millimeters.
2. Tolerance ±0.10mm (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

The name and content of poisonous and harmful material in products

Part's Name	Hazardous Substance					
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE
Limit	≤0.1%	≤0.1%	≤0.01%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○
Molding Compound	○	○	○	○	○	○
Chip	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○
Solder	×	○	○	○	○	○
Note	○：means the hazardous material is under the criterion of SJ/T11363-2006. ×：means the hazardous material exceeds the criterion of SJ/T11363-2006. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.					

Warnings

1. Exceeding the maximun ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximun ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessory to protect the device from being damaged by the static electricity when using it.
4. This publication is made by H&M Semiconductor and subject to regular change without notice.

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