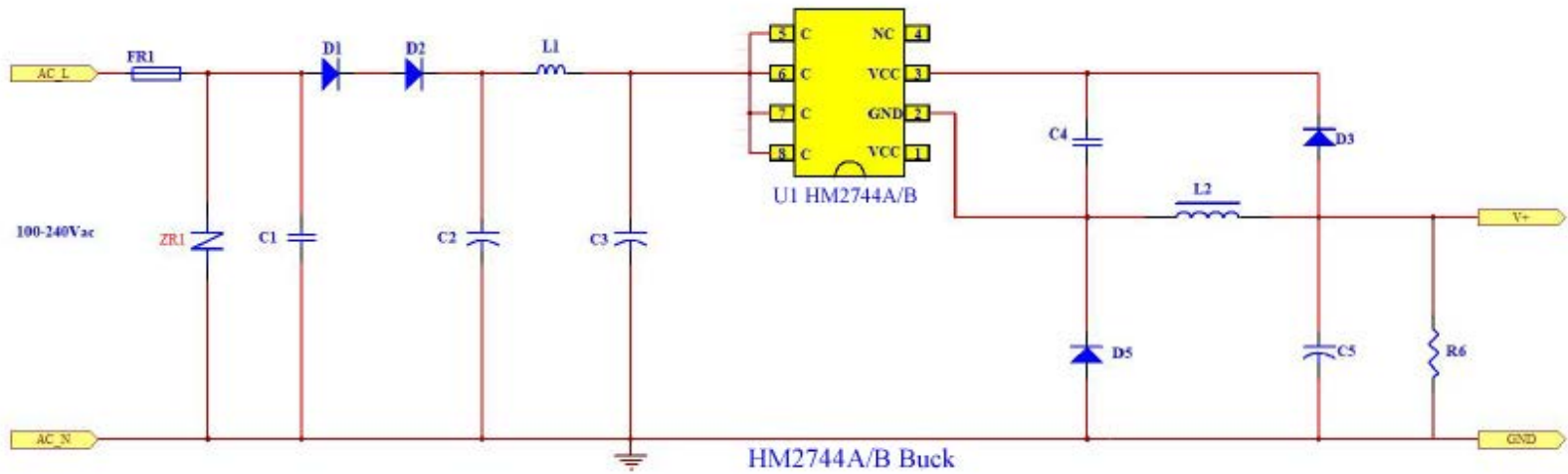
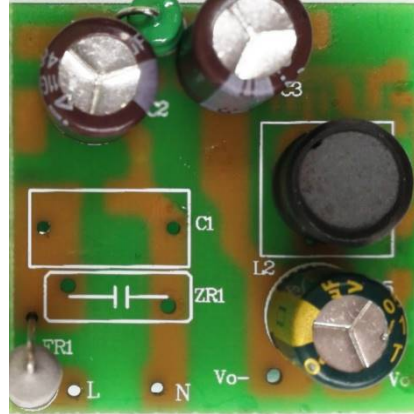


Description		Symbol	Min	Typ	Max	Units	Comment
<b>Input</b>							
<b>Voltage</b>		$V_{IN}$	<b>90</b>		<b>265</b>	<b>V<sub>AC</sub></b>	<b>2 Wire</b>
<b>Frequency</b>		$f_{LINE}$	<b>47</b>	<b>50/60</b>	<b>63</b>	<b>Hz</b>	
<b>No-load Input Power (230V<sub>AC</sub>)</b>					<b>100</b>	<b>mW</b>	
<b>Output</b>							
<b>Const Voltage</b>	<b>Output Voltage</b>	$V_{OUT\_CV}$	<b>4.6</b>	<b>5</b>	<b>5.4</b>	<b>V</b>	<b>Measured at the end of PCB</b>
	<b>Output Current</b>	$I_{OUT\_CV}$		<b>200</b>		<b>mA</b>	
<b>Output Ripple Voltage</b>		$V_{RIPPLE}$			<b>100</b>	<b>mV<sub>P_P</sub></b>	<b>Measured at the End of PCB With Rated Load @T<sub>A</sub> = 25 °C 20 MHz Bandwidth</b>
<b>Total Output Power</b>							
<b>Continuous Output Power</b>		$P_{OUT}$		<b>1</b>		<b>W</b>	
<b>Over Current Protection</b>		$I_{OUT\_MAX}$			<b>350</b>	<b>mA</b>	<b>Auto-restart</b>
<b>Active Mode Efficiency</b>		$\eta$	<b>55</b>			<b>%</b>	<b>Measured at PCB terminal, V<sub>IN</sub> = 115VAC (T<sub>AMB</sub> = 25 °C).</b>
<b>Environmental</b>							
<b>Conducted EMI</b>			<b>Meets CISPR14/ EN55014B</b>				
<b>Safety</b>			<b>Meets IEC60335</b>				
<b>Ambient Temperature</b>		$T_{AMB}$	<b>0</b>		<b>75</b>	<b>° C</b>	<b>Free convection, sea level, PCBA</b>

	Test Items	Spec	Remark
1	Standby Power	$\leq 100\text{mW}$	Pass
2	Efficiency	$\geq 55\%$	Pass
3	Output Voltage	4.60-5.40V	Pass
4	Dynamic	4.50-5.50V	Pass
5	Over Current Protection & Recovery	$\leq 350\text{mA}$	Pass
6	Ripple & Noise	$< 100\text{mV}$	Pass
7	Ambient Temp PCBA	$\leq 75^{\circ}\text{C}$	Pass

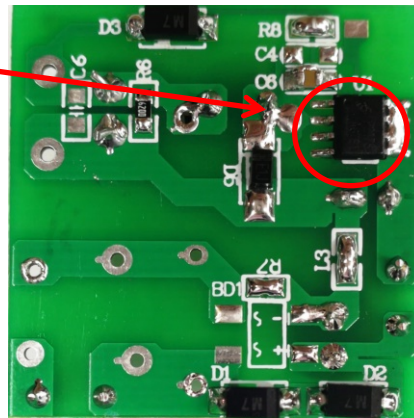


## 4. Circuit Board Photograph



Top side

HM2744A



Bottom side

编号	材料名称	型号规格	单位	位置符号	用量
1	贴片电阻	620R/0805 5%	PCS	R6	1
2	贴片电容	1uF/16V/0805 X7R 10%	PCS	C4	1
3	集成贴片IC	PT6110ESUP8	PCS	U1	1
4	贴片二极管	A7(1N4007) SOD-123	PCS	D1,D2,D3	3
5		ES1J(SF18) SOD-123	PCS	D5	1
6	保险丝电阻	线绕电阻 10R/1W	PCS	FR1	1
7	色环电感	1mH , 1W	PCS	L1	1
8	工字电感	1mH , DR8*10mm	PCS	L2	1
9	电解电容	2.2uF/400V $\phi$ 8*12mm	PCS	C2,C3	2
10		220uF/16V $\phi$ 8*12mm 高频低阻	PCS	C5	1
	总计				13
说明：					
雷击1.5KV以上需增加压敏电阻ZNR1,1.5KV可不选用。					

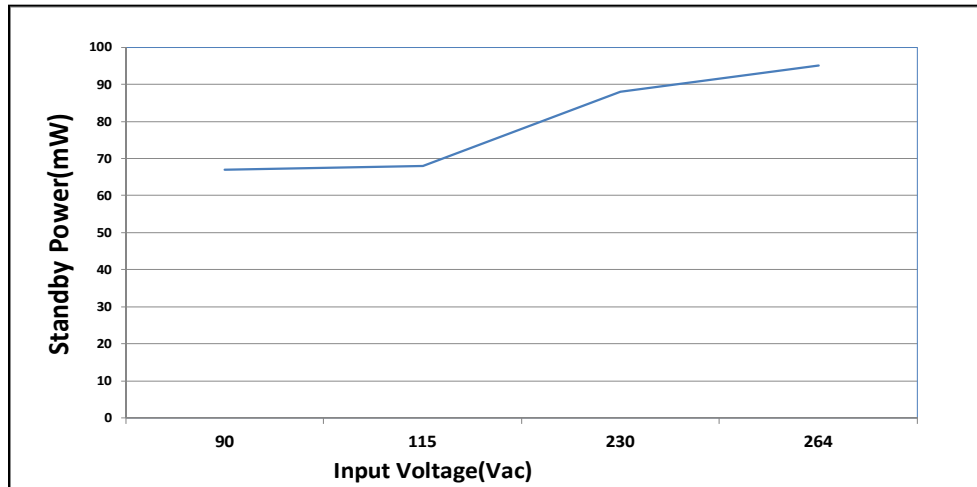
## 6. Ripple, Regulation and Efficiency

Item	Io(mA)	Vo(V)End of PCB	Pin(mW)	Eff(%)	Ripple(mV)	OCP(mA)
90V/60Hz	0	5.06	67		62.0	271
	50	4.99	410	60.85	52.0	
	100	4.96	755	65.70	60.0	
	150	4.94	1120	66.16	64.0	
	200	4.93	1520	64.87	70.0	
115V/60Hz	0	5.03	68		48.0	275
	50	4.98	409	60.88	52.0	
	100	4.94	750	65.87	60.0	
	150	4.93	1110	66.62	56.0	
	200	4.92	1510	65.17	40.0	
230V/50Hz	0	4.95	88		28.0	306
	50	4.95	444	55.74	52.0	
	100	4.92	787	62.52	64.0	
	150	4.91	1180	62.42	58.0	
	200	4.90	1590	61.64	40.0	
264V/50Hz	0	4.93	95		44.0	312
	50	4.93	462	53.35	59.0	
	100	4.92	810	60.74	65.0	
	150	4.90	1210	60.74	60.0	
	200	4.90	1620	60.49	62.0	

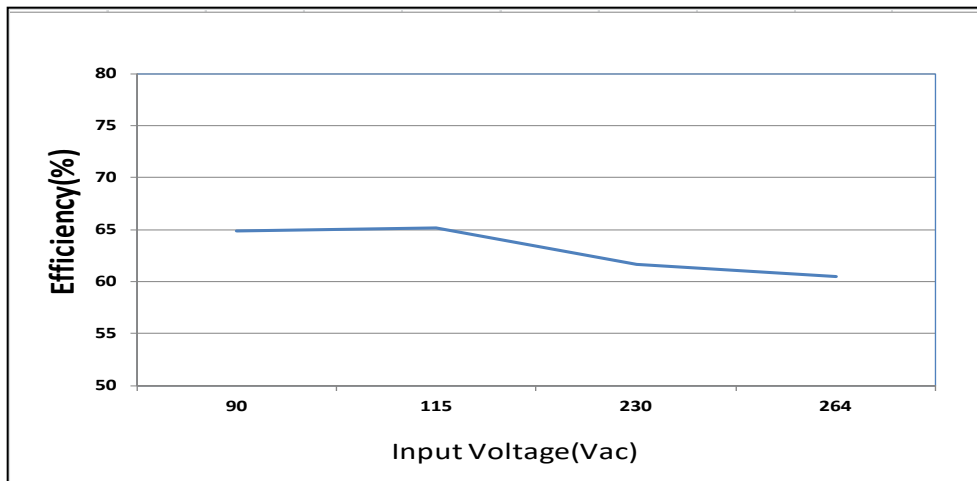
\* Note: Vout is measured at end of PCB.

# 7. Standby power and Efficiency

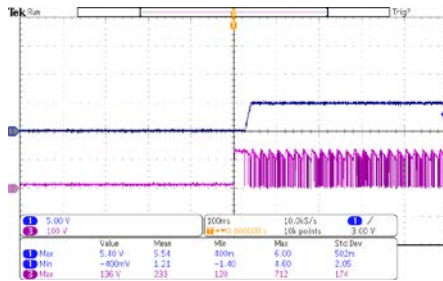
## Standby power



## Efficiency

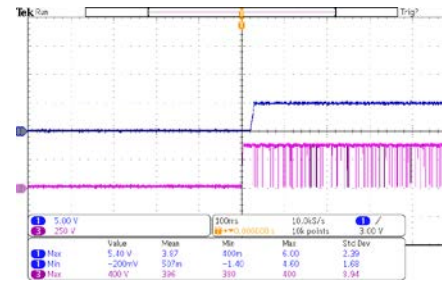


Vin=90Vac/50Hz, start up at full Load



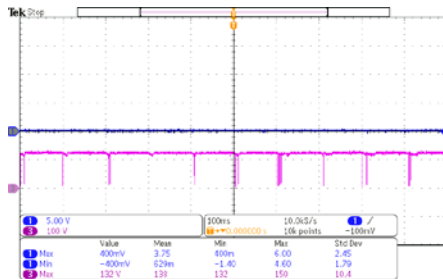
Vce Max=136V

Vin=264Vac/50Hz, start up at full Load



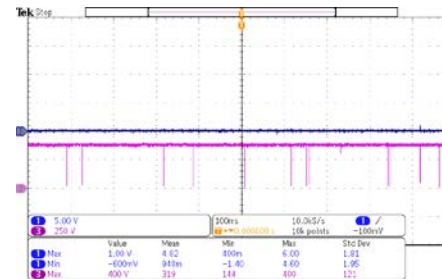
Vce Max=400V

Vin=90Vac/50Hz, output short



Vce Max=132V

Vin=264Vac/50Hz, output short



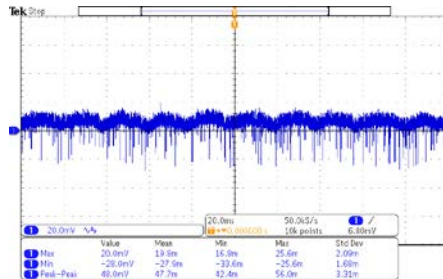
Vce Max=400V

**\* Note: Vce < 800V\*90%=720V**



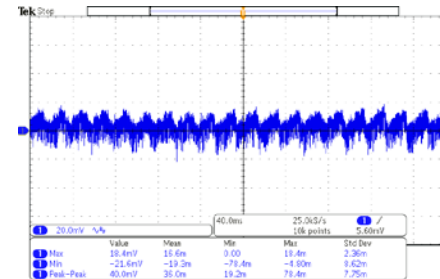
# 9. Ripple and Noise

Vin=115Vac/50Hz, No Load



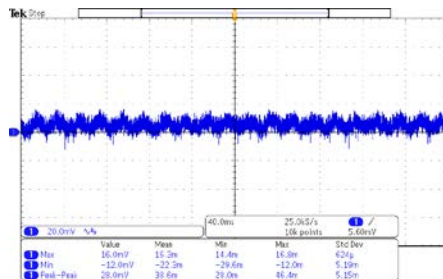
Vripple=48mV

Vin=115Vac/50Hz, Full Load



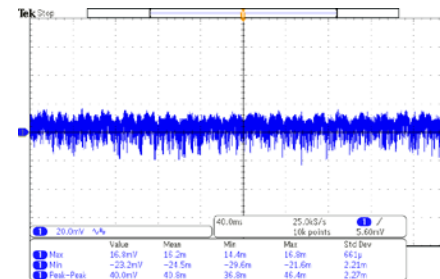
Vripple=40mV

Vin=230Vac/50Hz, No load



Vripple=28mV

Vin=230Vac/50Hz, Full load

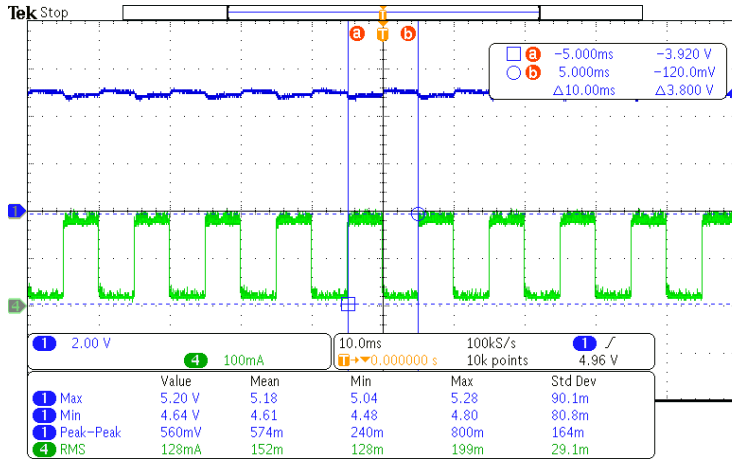


Vripple=40mV

**\* Note:  $V_{RIPPLE} < 100mV$**

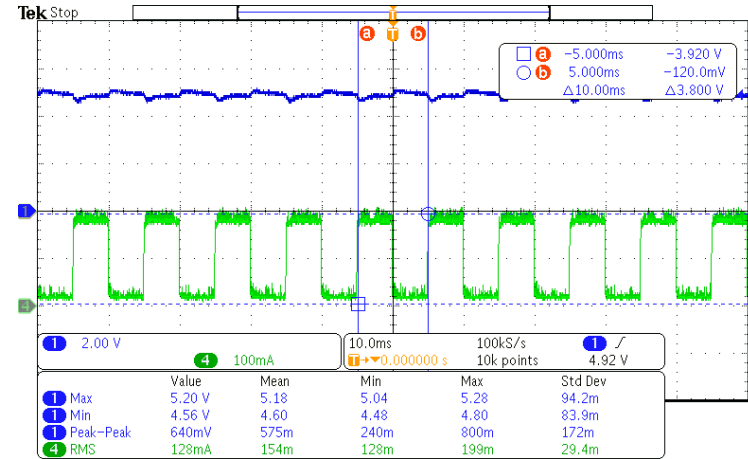
# 10. Dynamic Load

Vin=90Vac/60Hz, Io from 10% to 90%  
Slew rate:255mA/uS, F=100Hz



Vout=4.64-5.20V

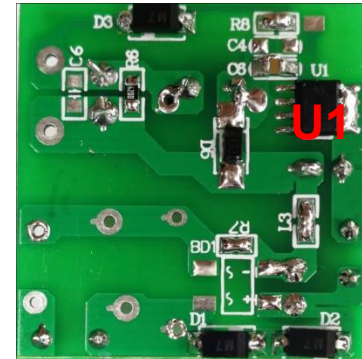
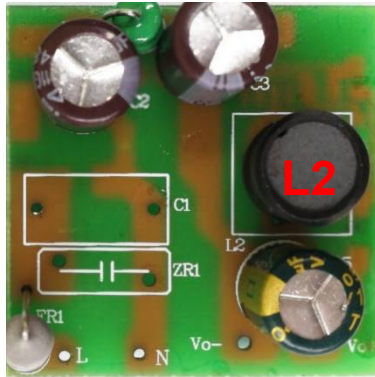
Vin=264Vac/50Hz, Io from 10% to 90%  
Slew rate:255mA/uS, F=100Hz



Vout=4.56-5.20V

• Note: 4.50V < Vout < 5.50V, Vout measured at end of PCB.

# 11. Component Thermal



Item	Thermal (°C)					
	90Vac/Full Load		230Vac/Full Load		265Vac/Full Load	
	T	$\Delta T$	T	$\Delta T$	T	$\Delta T$
U1-PT G I I Æ	94	19	101	26	110	35
D5-ES1J	94	19	96.5	21.5	100	25
L2-DR8*10	90	15	91.5	16.5	90.5	15.5
Ambient Temp	75°C					

**\* Note:  $T_{L2 \& U1} < 120^{\circ}\text{C}$  , Others are within the specification.**